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IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

INVENTORS:

Thomas J. Krutsick

CASE: 5

TITLE: FIELD PLATED RESISTOR WITH ENHANCED
ROUTING AREA THEREOVER

SERIAL NO. 09/650,604

FILING DATE 04/18/00

COMMISSIONER FOR PATENTS
ALEXANDRIA, VA 22313-1450

SIR:

Please find enclosed an IDS with citations from the European Patent Office.

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Respectfully,

Peter V.D. Wilde
Attorney for Applicants
Reg. No. 19658

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| | | Application Number | 09/650,604 | | |
| | | Filing Date | 04/18/00 | | |
| | | First Named Inventor | Krutsick | | |
| | | Art Unit | 2826 | | |
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| | | PATENT ABSTRACTS OF JAPAN, vol. 0061, no. 85 (E-132) 09/21/1982 | |
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